

SEMICONDUCTOR DEVICE ADAPTED FOR FORMING MULTIPLE SCAN CHAINS

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ABSTRACT

A device includes data input and data output pins such as found in a JTAG port and a first plurality of boundary scan cells. The device is configurable to support a secondary boundary scan cells formed from the first plurality of
10 boundary scan cells and a second plurality of boundary scan cells in at least one external device. In one embodiment, the device includes a demultiplexer which may be configured to support a primary boundary scan chain between the JTAG port and the first plurality of boundary scan cells. The demultiplexer may also be configured to support the secondary boundary scan chain between the JTAG port
15 and the first and the second plurality of boundary scan cells.